Tel: 1-800-722-8197

**EMAIL: SALES@CHALLELEC.COM** 1-631-595-2217 WEB: WWW.CHALLENGEELECTRONICS.COM

ISO 9001:2008; ISO-TS 16949:2009 Certified Fax: 1-631-667-5484

# PRODUCT INFORMATION

**Part Numbers** 

CM06M-03S28-BD-1

Legacy # CEM-DB60221-283AD-L01CAR-00-0

Revision

0-2019

**Type** 

**Omni-Directional Digital Electret Condenser Microphone** 

**Description** 

This DECM (Digital electret condenser microphone) and an ECM consists of a preamplifier, a delta sigma modulator ,and on chip voltage regulator. The A/D conversion is performed by a 4<sup>th</sup> order  $\Delta\Sigma$  modulator.



Compliance

RoHS, Lead Free

9001:2000



| Date | ECN# | Rev # | Description | Page | Prepared By | Approved By |
|------|------|-------|-------------|------|-------------|-------------|
|      |      |       |             |      |             |             |
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### 1. Electrical Characteristics

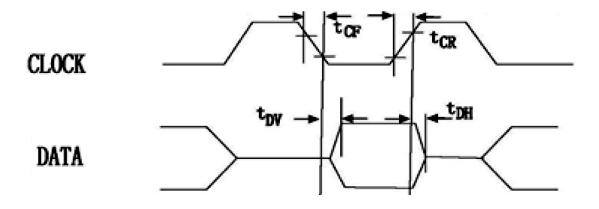
This DECM (Digital electret condenser microphone) and an ECM consists of a preamplifier ,a delta sigma modulator ,and on chip voltage regulator. The A/D conversion is performed by a  $4^{th}$  order  $\Delta\Sigma$  modulator.

| No   | Parameter                           | Symbol | Min        | Тур             | Max        | Unit | Comments                                |
|------|-------------------------------------|--------|------------|-----------------|------------|------|---|
| 1.1  | Sensitivity(analog signals output)  | s      | -31        | -28             | -25        | dB   | 0dB=1V/Pa, at 1kHz                      |
| 1.2  | 1.2 Noise transfer function cut-off |        |            | 0.055 x<br>fCLK |            | MHz  | Relative to fCLK                        |
| 1.3  | Modulator order                     |        |            | 4               |            |      | Given by design                         |
| 1.4  | Idle mode tone frequency            | fT     | 22         |                 |            | KHz  | @ Fclk=1 MHz                            |
| 1.5  | Clock freq.(sample rate)            | fCLK   | 1          | 2.4             | 3          | MHz  |   |
| 1.6  | Clock duty cycle                    | fDC    | 40         | 50              | 60         | %    |   |
| 1.7  | Jitter tolerance                    | δ      |            |                 | 0.5        | ns   |   |
| 1.8  | Output Voltage low                  | VIOL   | -0.3       |                 | 0.35 X VDD | V    |   |
| 1.9  | Output Voltage high                 | VIOH   | 0.65 X VDD |                 | VDD+0.3    | V    |   |
| 1.10 | Output current at high voltage      | IH     | 1          |                 | 10         | mA   | Short circuit current                   |
| 1.11 | Extended Vdd range                  |        | 2.4        | 3.3             | 3.63       | V    |   |
| 1.12 | Signal to Noise Ratio               | S/N    | 57         |                 |            | dB   | at 1kHz S.P.L=1Pa<br>(A-Weighted Curve) |

## 2. Digital Logic Characteristics

| Symbol           | Parameter                              | Min                  | Typical | Max                  | Units |
|------------------|--|----------------------|---------|----------------------|-------|
| V <sub>IT+</sub> | Positive-going input threshold voltage |                      | 1.82    |                      | V     |
| $V_{IT-}$        | Negative-going input threshold voltage |                      | 1.27    |                      | V     |
| $\Delta V_{IT}$  | Input hysteresis                       |                      | 0.55    |                      | V     |
| $V_{IOL}$        | Data input/output logic low level      | -0.3                 |         | 0.35*V <sub>DD</sub> | V     |
| $V_{IOH}$        | Data input/output logic high level     | 0.65*V <sub>DD</sub> |         | V <sub>DD</sub> +0.3 | V     |

| Ī |      | High Impedance | Data sampled at |  |  |
|---|------|----------------|-----------------|--|--|
|   | DATA | Falling clock  | Rising clock    |  |  |



Timing diagram of CLK and DATA signals

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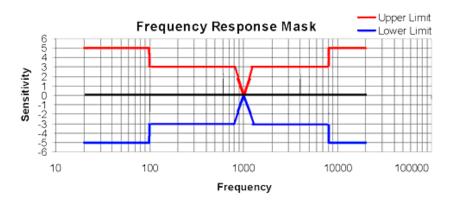
## 3. Frequency Response Curve

The microphone must fulfill the relative frequency response tolerance window specifications with the following measurement conditions.

- TEMPERATURE: +20°C
- ACOUSTIC STIMULUS: 1Pa (94dB SPL) measured at 50 cm from the Hi-Fi loudspeaker. The loudspeaker must be equalized for flat frequency response.
- POSITION: The far field measurement point is located 50cm from the Hi-Fi speaker. The speaker must be positioned away from any reflecting surfaces. The 1Pa acoustic stimulus is at the microphone position.

#### Frequency Response Mask for Digital microphones

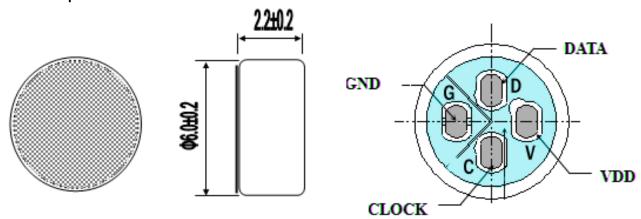
| Frequency / Hz | Lower limit | Upper limit | Unit     |
|----------------|-------------|-------------|----------|
| 20 100         | -5          | +5          | dBr 1kHz |
| 100 8000       | -3          | +3          | dBr 1kHz |
| 800020 000     | -5          | +5          | dBr 1kHz |



NOTE: The distribution of the sensitivity must be a Normal Distribution and the Cpk value for the sensitivity must be at least 1.66 in all conditions.

#### **Mechanical Characteristics**

- 4.1. Weight: 0.3 grams
- 4.2. All dimensions in millimeter (mm).
- 4.3. Tolerance: as specified.
- 4.4. Microphone Dimensions: 6.0 mm x 2.2 mm



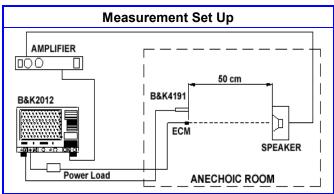
| Name |          | Function                                   |  |
|------|----------|--|--|
|      | GND      | Ground                                     |  |
| CLK  |          | User-adjustable clock input to microphone  |  |
| DATA |          | Microphone PDM data output                 |  |
|      | $V_{DD}$ | Power supply and IO voltage for microphone |  |

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#### **Measurement Circuit**



## **Measurement Conditions**

In Normal Weather **Environment Temperature:** 5~+35°C

Relative Humidity: 45 ~ 85% Air Pressure: 86 ~ 106Kpa

In Arbitrate Weather

**Environment Temperature:** 20±2°C Relative Humidity: 60 ~ 70% Air Pressure: 86 ~ 106Kpa

#### **Testing Procedure**

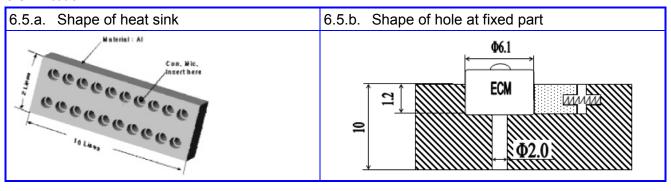
- 1. Measure the microphones under standard operating condition.
- 2. Put the microphone and standard microphone face to the sound source (speaker), the distance between sound source and microphone & standard microphone is 50 cm. And keep the center distance 5cm between them to ensure that the change of sound pressure should be kept within ± 1 dB.
- 3. Keep the sound source pressure within ± 1 dB from speaker Measured by standard microphone.
- 4. The sensitivity of microphone can obtain its output voltage when sound source kept within 1,000 Hz. & 0.1 Pa.

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## Soldering Condition

- 6.1. We suggest using anti-static welding machine which can control soldering temperature automatically.
- 6.2. Soldering temperature should be controlled under 320°C and soldering time for each terminal should be 1~2 seconds
- **6.3.** Microphone should be fixed on the metal block (heat sink), which has high radiation effects, and heat sink shall contact with MIC tightly.
- **6.4.** Microphone may easily be destroyed by the static electricity and the countermeasure for eliminating the static electricity shall be executed (worktable and human body shall be ground connection)
- **6.5.** Heat Sink



## 7. Reliability

| Thermal Operating  | 240 hours continuous operation at Rated Voltage, at Maximum Rated Operating Temperature *   |  |  |  |  |  |  |  |
|--|---|--|--|--|--|--|--|--|
| Temperature Test   | 240 hours continuous operation at Rated Voltage, at Minimum Rated Operating Temperature *   |  |  |  |  |  |  |  |
| Thermal Storage Temperature  | 96 hours storage at Maximum Rated Storage Temperatures *  |  |  |  |  |  |  |  |
| Test   | 96 hours storage at Minimum Rated Storage Temperatures *  |  |  |  |  |  |  |  |
|  | SINGLE CYCLE  |  |  |  |  |  |  |  |
| Temperature Shock  | 30 cycles of Minimum and Maximum Operating Temperature Each cycle shell be set per diagram and is 3 hours long *  Maximum *C  25*C  Minutes  Minimum *C  Minimum *C   |  |  |  |  |  |  |  |
| Highly Accelerated<br>Temperature and Humidity<br>Stress Test (HAST) | The DUTs are soldered onto a test PCB. The PCB is placed in the oven and the input terminal leads are brought out and connected to the electrical power supply. The test PCB is powered up using the electrical power supply that is designed to maintain a constant voltage of the maximum rated voltage for the duration of the test. The oven is programmed to maintain a temperature of 110°C, 85% RH for 264 hours with a pressure of 122 KPa introduced * |  |  |  |  |  |  |  |
| <b>Humidity Test</b>   | Precondition at +25°C for <b>1 hour</b> . Then expose to +70°C with 90 to 95% relative humidity for <b>120 hours</b> *  |  |  |  |  |  |  |  |
| ESD Sensitivity  | Perform ESD sensitivity threshold measurements for each contact according to MIL-STD-883G, Method 3,015.7 for Human Body Model. Identify the ESD threshold levels indicating passage of 8,000V Human Body Model. *  |  |  |  |  |  |  |  |
| Termination Strength   | Maximum of 9.8 N (1.0 Kg) load pull test, applied to each terminal in axial direction for 1 minute  |  |  |  |  |  |  |  |
| Drop Test  | Samples are mounted on a test PCB. The PCB itself was then glued onto a metal fixture slightly bigger than the PCB. The fixture was dropped naturally from the 1.5 m height onto a steel surface. The test was repeated in six directions for three times, total 18 times and inspected for mechanical damage *   |  |  |  |  |  |  |  |
| Random Vibrations  | Vibrate randomly along three perpendicular directions for <b>30 minutes in each direction</b> . The Power Spectral Density of the vibration had a + 3 dB/octave rise from 20 Hz to 80 Hz, a constant value of 0.053 g2/Hz from 80 Hz. to 350 Hz and finally a -3 dB/octave drop from 350 Hz to 2,000 Hz. Having subjected the units to vibration, they were tested for all electrical and acoustic parameters   |  |  |  |  |  |  |  |
| Mechanical Shock   | Subject samples to half sine shock pulses (3,000 g's ±15% for 0.3 ms) in each direction, totally <b>6 shocks</b>  |  |  |  |  |  |  |  |
| Sinusoidal Vibration   | Vibrate randomly from 10 Hz to 55 Hz, 1 octave/minute with 2 mm amplitude (peak to peak) for <b>2 hours in each direction</b> For this test are that the PCB must deflect at least 1mm before mechanical failure of the unit occurs   |  |  |  |  |  |  |  |
| Life Test  | Subject samples to +125°C for <b>168 hours</b> under full maximum rated voltage   |  |  |  |  |  |  |  |
| * Reliability Test Performance                                       | Parts should conform to original performance within ±3 dB, after 3 hours of recovery period   |  |  |  |  |  |  |  |

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## 8. Concept of Unit

The difference between concept of "PASCAL" unit and the one unit "µbar" can be explained as follows: In calibrating the sensitivity of ECMs, the sensitivity is manifested differently according as the units "PASCAL" or "µbar". For example:

 $-60 \text{ dB} (0 \text{ dB} = 1 \text{ V} / \mu \text{bar}) - -42 \text{ dB} (0 \text{ dB} = 1 \text{ V} / Pa)$ 

### 9. Construction Materials

| ITEM | PART NAME           | MATERIAL     | QTY | 8_6 |
|------|---------------------|--------------|-----|-----|
| 10   | Chip Capacitor      | 100000PF     | 1   |     |
| 9    | IC                  |              | 1   | 7   |
| 8    | P.C.B               | FR-4         | 1   | 6   |
| 7    | Copper Ring         |              | 1   | 5   |
| 6    | HOUSING CHAMBER     |              | 1   |     |
| 5    | ELECTRET BACK       |              | 1   | 4   |
| 4    | SPACER              |              | 1   | 3   |
| 3    | POLARIZED DIAPHRAGM |              | 1   | 10  |
| 2    | CASE                | AL-mg alloy  | 1   | 2   |
| 1    | FELT                | Fabric cloth | 1   |     |

## 10. Part Number Description

| Code | Description                      |
|------|----------------------------------|
| CEM  | Challenge Electronics Microphone |
| -    | dash                             |
| D    | Digital                          |
| В    | Back Electret                    |
| 60   | 6.0 mm diameter                  |
| 22   | 2.2 mm height                    |
| 1    | PCB Type                         |
| -    | dash                             |
| 283  | Sensitivity 28± 3 dB             |
| Α    |                                  |
| D    |                                  |
| -    | dash                             |
| L01  | Solder termination               |
| CAR  | Capacitor                        |
| -    | dash                             |
| 00   |                                  |
| -    | dash                             |
| 0    |                                  |

## 11. Warranty

For a period of one (1) year from date of shipping under normal handling and operations conditions This warranty does not apply to products damaged through misuse, abuse, improper installation, alteration, rework, or attempt to repair Tel: 1-800-722-8197

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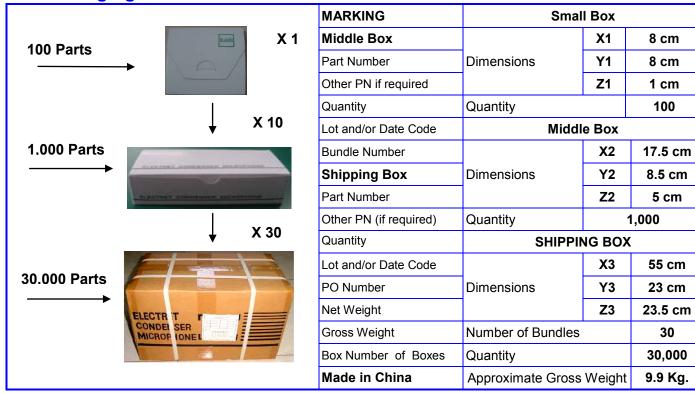
Date 11/14/2019

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## 12. Packaging

Revision

0-2019



Description

Revised PN CEM-DB60221-283AD-L01CAR-00-0